

EL 465686283

DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated
below next to my name.

I believe I am the original, first and joint inventor of the subject
matter which is claimed and for which a patent is sought on the
invention entitled: Transistor Structures, Methods of Incorporating
Nitrogen into Silicon-Oxide-Containing Layers; and Methods of Forming
Transistors, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents
of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be
material to patentability as defined in Title 37, Code of Federal
Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's
certificates have been filed prior to the date of execution of this
declaration.

I hereby declare that all statements made herein of my own
knowledge are true and that all statements made on information and
belief are believed to be true; and further that these statements were
made with the knowledge that willful false statements and the like so

1 made are punishable by fine or imprisonment, or both, under
2 Section 1001 of Title 18 of the United States Code and that such willful
3 false statement may jeopardize the validity of the application or any
4 patent issued therefrom.

5 * * * * *

6 Full name of inventor: Gurtej S. Sandhu

7 Inventor's Signature: Gurtej Singh Sandhu

8 Date: 7/21/03

9 Residence: Boise, ID

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15 Full name of inventor: John T. Moore

16 Inventor's Signature: John T. Moore

17 Date: 7/21/03

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* * * * *

Full name of inventor: **Neal R. Rueger**

Inventor's Signature: Neal R. Rueger

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